

内置升压的77W单体声D类音频功放

77W Class D Audio Amplifier with Boost Converter

■ FEATURES

- Output Power ($f_{IN} = 1\text{kHz}$, $R_L = 4\Omega$, BTL)
 $V_{BAT} = 7.4\text{V}$, 44W ($V_{OUT} = 18\text{V}$, THD+N = 10%)
 $V_{BAT} = 12\text{V}$, 77W ($V_{OUT} = 24\text{V}$, THD+N = 10%)
- Power Supply V_{BAT} : 2.7V~ V_{OUT}
- Spread Switching Frequency Function for AMP
- Differential / Single-ended Analog Input, BTL Output
- Integrated boost converter: adjustable output voltage V_{OUT} and switch peak current limit
- Over current protection/ Over temperature protection / Overvoltage protection / Low voltage malfunction prevention function with auto recovery
- Pb-free Packages, ETSSOP28 (EP UP)
- 输出功率($f_{IN}=1\text{kHz}$, $R_L=4\Omega$)
双锂电7.4V: 44W ($V_{OUT}=18\text{V}$, THD+N= 10%)
三锂电12V: 77W ($V_{OUT}=24\text{V}$, THD+N = 10%)
- V_{BAT} 供电范围: 2.7V至 V_{OUT}
- D类功放扩频功能
- 模拟差分/单端输入
- 内置升压电路: 可调节的升压值和升压限流
- 保护功能: 过流/过热/欠压异常/过压保护功能
- 无铅无卤封装, ETSSOP28 (EP UP)

■ APPLICATIONS

- Smart Speakers
- Portable Speakers
- Megaphone
- Wireless Speakers
- 2.1Channel Speakers
- Portable Gamers
- 智能音响
- 无线音响
- 2.1声道小音箱
- 拉杆音箱
- 便携式音箱
- 便携式游戏机

■ DESCRIPTION

The HTA8127 is a mono Class D audio amplifier integrated a boost converter. With wide input voltage range, HTA8127 supports applications with single cell, two cell Lithium batteries, 5V or 12V power supply and so on, and can drive one speaker in BTL mode.

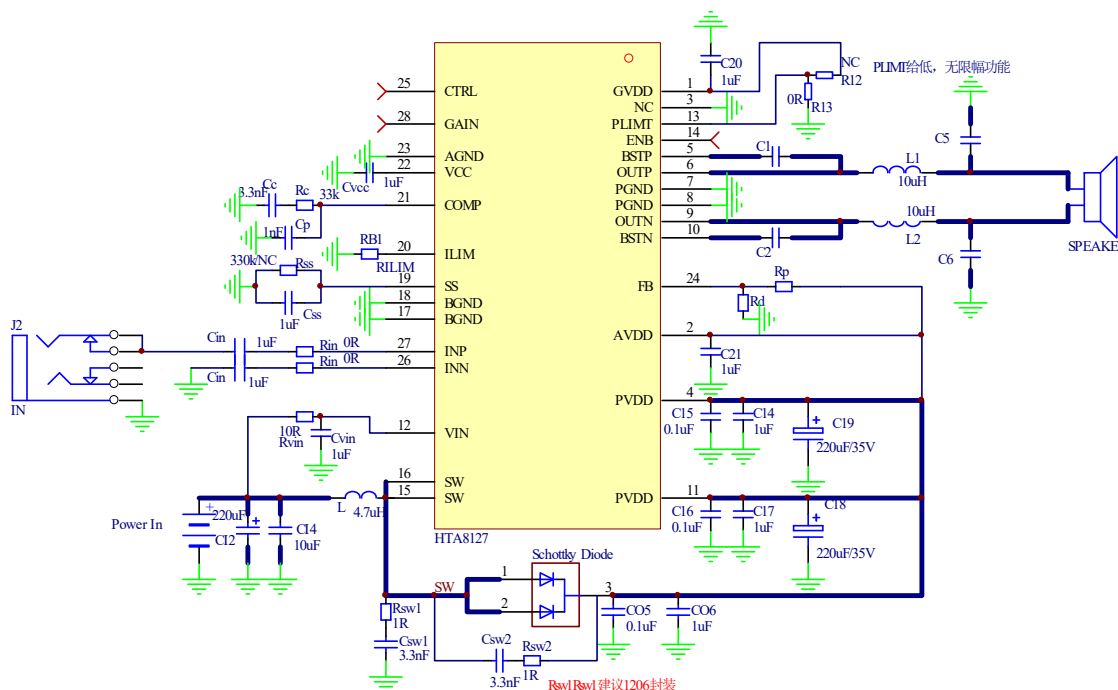
HTA8127 built-in boost converter supports selectable output voltage by external resistors for different applications with different power requirements. It also implements an adjustable switching peak current limit function through external resistors.

HTA8128 has a filter-less modulation circuit which can directly drive speakers. HTA8128 can be shut down so that the power consumption can be minimized. As for protection function, over current protection function for speaker output terminals, over temperature protection function, low supply voltage malfunction preventing function and over boost voltage protection are also prepared.

HTA8127是一款内置升压的单声道D类音频功率放大器, 其支持单节锂电、双节锂电串联、5V、12V等多种输入, 升压后的电压提供给功放供电, 功放支持单声道BTL输出。

HTA8127内置的升压电路, 可通过FB脚设置升压值, 以满足不同的输出功率需求。其还可通过外置电阻调节开关峰值电流限值。

此外, HTA8127内部集成免滤波器调制技术, 能够直接驱动扬声器, 内置的关断功能使待机电流最小化, 还集成了输出端过流保护、片内过温保护、输入电源欠压异常保护、升压电压过压保护等功能。

TYPICAL APPLICATION


1. CTRL, ENB分别控制功放部分、升压部分的使能。

CTRL=H: 功放工作（有两种工作方式，如下表）；CTRL=L: 功放关断。

V_{ENA}	Working Mode
<0.5V	功放关断 Amp disabled
1.2V~1.7V	功放工作，扩频功能开启 Spread spectrum Enabled
2.3~5.5V	功放工作，扩频功能关闭 Spread spectrum Disabled

ENB=H: 升压工作；ENB=L: 不升压。

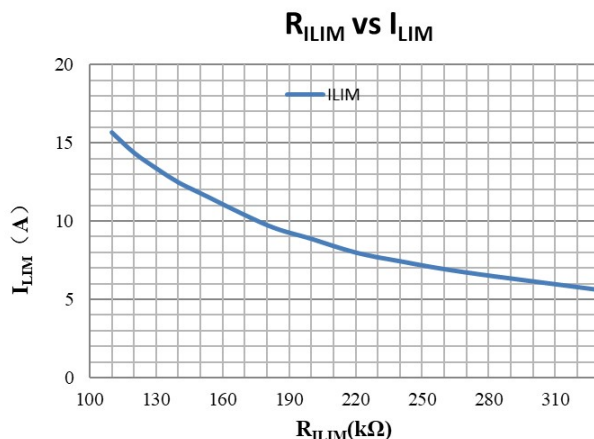
建议上电时序：Power In上电，延时10ms后，ENB拉高，延时150ms后，CTRL拉高；若CTRL与ENB短接在一起拉高，则建议SS电容减小为100nF。

2. 升压电压设置，通过FB端Rp和Rd电阻。计算公式、推荐设置，如下

$$V_{OUT} = 1.204 \times \left(1 + \frac{R_P}{R_D}\right)$$

$V_{OUT}(V)$	$R_P(\Omega)$	$R_D(\Omega)$
7.4	510k	100k
9	520k	82k
12.5	520k	56k
14.8	510k	47k
18.2	510k	36k
22.1	470k	27k
24	510k	27k

3. 升压部分电感峰值电流 I_{LIM} 设置，通过ILIM pin下地电阻 R_{LIM} ，可参考下图取值， I_{LIM} 设置建议小于电源输入端最大电流能力，并设置大于 $1.4 \times \frac{P_{O,peak}}{V_{IN} \times 70\%}$



4. 升压部分电感推荐使用4.7uH，其饱和电流I_{SAT} > I_{LIM}
5. 升压部分肖特基二极管推荐使用2*SS54，其V_{RMS} > 1.5*V_{OUT}，I_{F(AV)} > I_{OUTmax}
6. SW端预留两组RC (R_{sw1}, C_{sw1}; R_{sw2}, C_{sw2})，并根据SW峰值电压和EMI表现，酌情添加。电阻推荐使用1206封装。RC因尽量靠近SW引脚放置，R_{sw1}, C_{sw1}连接到地与芯片地应保持最短路径。
7. SS端设置升压的软起动时间，以及SW端的tr/tf时间

其中到地的C_{SS}电容设置软起动时间，建议使用1uF电容。软起动时间 $t_{SS}(ms) \approx \frac{1.204 \times C_{SS}(nF)}{5}$

到地的电阻R_{SS}设置tr/tf时间，不接R_{SS}时，SW端tr/tf更陡，此时芯片效率更高，但EMI表现更差；当R_{SS}=330k时，tr/tf更缓，此时芯片效率更低，但EMI表现更好。

8. Comp端设置升压部分的补偿电路，一般使用推荐的R_c = 33k, C_c = 3.3nF, C_p = 1nF。如出现输出不稳定、THD+N异常等环路不稳定导致的异常，可调整Comp参数，具体参考使用我司excel计算工具。
9. 升压输入端VBAT，建议加入10uF//220uF的并联电容到地；升压输出电压端(肖特基二极管端)，建议放置0.1uF//1uF的并联电容到地，其使用粗线短路径连接到两个PVDD，在靠近各自引脚加入0.1uF//1uF//220uF的并联电容到地，电容的额定电压需留有足够的余量，电容地与芯片地应保持最短路径。
10. 音频输入端，可接入单端或差分信号，上图按单端信号示意接入。其中GAIN设置系统增益，R_{IN}影响系统增益，C_{IN}影响低频频响。

GAIN = H时，系统增益 $Gain(dB) \approx 20 \times \log\left(\frac{250k}{10k+R_{IN}}\right)$,

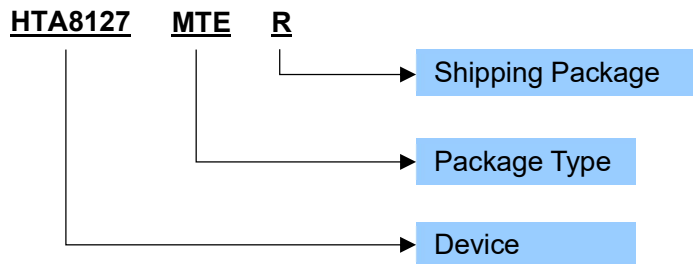
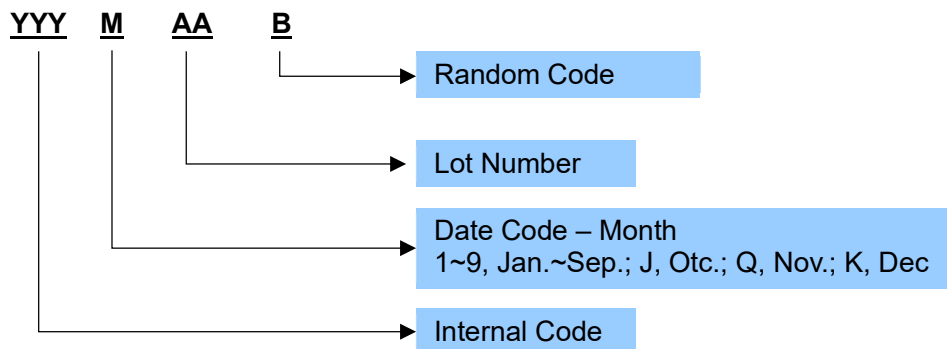
GAIN = L时，系统增益 $Gain(dB) \approx 20 \times \log\left(\frac{630k}{10k+R_{IN}}\right)$,

高通滤波器（衰减低频）截止频率 $f_c(-3dB) = \frac{1}{2\pi(10k+R_{IN}) \times C_{IN}}$

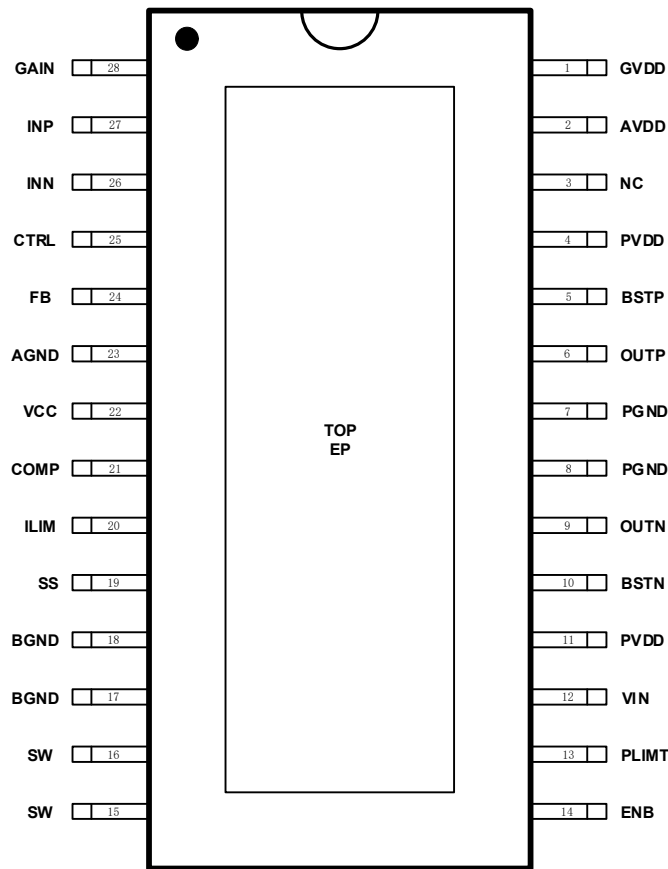
11. 音频输出端，可使用磁珠或电感作为滤波器。使用磁珠时，使用>200R@100MHz的磁珠，配合使用1nF的下地电容；使用电感时，使用10uH+0.68uF的组合（有高频频响平滑度要求时，需配合负载阻抗使用不同的LC，具体参考使用我司excel计算工具。
12. PLIMIT引脚，设置功放输出的功率限幅，限幅值约为52.33-20.2*V_{PLIMIT} (V)，其中V_{PLIMIT} (V)即为PLIMIT引脚从GVDD分到的电压。如果PLIMIT引脚连接到GND，PLIMIT功能关闭。

ORDERING INFORMATION

Part Number	Package Type	Marking	Operating Temperature Range	Shipping Package / MOQ
HTA8127MTER	ETSSOP28 (EP UP)	HTA8127 YYYMAAB ¹	-40°C ~ 85°C	Tape and Reel 3000PCS

Part Number

Production Tracking Code

¹ YYYMAAB is production tracking code

■ TERMINAL CONFIGURATION



Top View

■ TERMINAL FUNCTION

Terminal No.	Name	I/O ¹	Description
EP	--	-	Thermal pad up, must be good connected to heat-sink for power dissipation. 朝上的散热 PAD，需外接散热器。
28	GAIN	I	Gain selection terminal. 增益选择引脚
27	INP	I	Positive input terminal. 音频输入正端
26	INN	I	Negative input terminal. 音频输入负端
25	CTRL	I	Enable logic input and operation mode selection for amplifier. 功放使能与模式选择端
24	FB	I	Voltage feedback. 电压反馈脚
23	AGND		Analog Ground. 模拟地
22	VCC	O	Output of the internal regulator for boost converter. A ceramic capacitor of 1uF is required between this pin and ground. 升压电路内部电压输出，接 1uF 到底。
21	COMP	O	Output of the internal error amplifier, the loop compensation network should be connected between this pin and the AGND pin. 内部补偿脚
20	ILIM	I	Adjustable switch peak current limit. An external resistor should be connected between this pin and GND.

¹ I: Input; O: Output; G: Ground; P: Power

			最大限流值设置端，外部接电阻到地。
19	SS	O	Soft-start programming pin. An external capacitor connected to ground sets the ramp rate of the internal error amplifier's reference voltage during soft-start. 升压软启动设置脚，接电容到地。 Also used as mode selection for different tr/ff, an external resistor connected to ground selects a flatter tr/ff. 同时作为 tr/ff 设置脚，当同时外接 1 个 330k 电阻到地时，选择较缓的 tr/ff。
18,17	BGND	G	Boost converter ground. 升压电路地。
16,15	SW	P	The switching node pin of the converter. 升压开关点
14	ENB	I	Enable logic input for boost converter. Logic high level enables the device. Logic low level disables the device and turns it into bypass mode. 升压使能脚，高电平时升压开启，低电平时升压关闭
13	PLIMIT	I	Sets the maximum output voltage before clipping (Limiter Level) for PLIMIT function. 设置 PLIMIT 功能下的最大输出削顶电压（限幅值）
12	VIN	I	Power supply input for boost converter. 升压部分供电输入端。
11	PVDD	P	Power supply terminal for amplifier. 功放功率电源端。
10	BSTN	BST	Connection point for the OUTN bootstrap capacitor, which is used to create a power supply for the high-side gate drive for OUTN
9	OUTN	O	Negative pin for differential speaker amplifier output. 喇叭输出负端
8,7	PGND	G	Power Ground. 功率地
6	OUTP	O	Positive pin for differential speaker amplifier output. 喇叭输出正端
5	BSTP	BST	Connection point for the OUTP bootstrap capacitor, which is used to create a power supply for the high-side gate drive for OUTP
4	PVDD	P	Power supply terminal for amplifier. 功放功率电源端。
3	NC	--	No connection. 内部无连接
2	AVDD	P	Analog power supply. 模拟电源端。
1	GVDD	O	Voltage regulator of amplifier, connect 1uF to GND. 功放内部电压，接 1uF 到地

■ SPECIFICATIONS¹

● Absolute Maximum Ratings²

PARAMETER		Symbol	MIN	MAX	UNIT
Voltage range	PVDD	PVDD	-0.3	28	V
	SW	V _{SW}	-0.3	34	V
	VIN	V _{IN}	-0.3	18	
	INP, INN, CTRL, GAIN, PLIMT	V _I	-0.3	5.8	V
	ENB, VCC, SS, COMP	V _I	-0.3	7	V
	ILIM, FB	V _I	-0.3	3.6	V
Operating temperature range		T _A	-40	85	°C
Operating junction temperature range		T _J	-40	150	°C
Storage temperature range		T _{STG}	-50	150	°C

● Recommended Operating Condition

PARAMETER	Symbol	CONDITION	MIN	TYP	MAX	UNIT
VIN supply voltage range	V _{IN}		2.7		16	V
Output voltage range (PVDD)	V _{OUT}		4.5		24	V
High-level input voltage of CTRL, spread spectrum disabled	V _{ENAH}		2.3		5.5	V
Middle-level input voltage of CTRL, spread spectrum enabled	V _{ENAM}		1.2		1.7	V
Low-level input voltage of CTRL, amplifier shutdown	V _{ENAL}		0	0	0.5	V
GAIN selection	V _{GAIN1}	GAIN = L			1.0	V
	V _{GAIN2}	GAIN = H	2.0			V
High-level input voltage of ENB	V _{ENBH}		1.5		5.5	V
Low-level input voltage of ENB	V _{ENBL}		0		0.4	V
ENB internal pull-down resistor	R _{ENB}			800		kΩ
Inductance	L			4.7		μH
Operating temperature	T _a		-40	25	85	°C
Load impedance	R _L	BTL		4		Ω

● Electrical Characteristics

Boost Converter

PARAMETER	Symbol	CONDITION	MIN	TYP	MAX	UNIT
Output voltage range	V _{OUT}		4.5		24	V
Output overvoltage protection	V _{OVP}			28		V
Reference voltage at the FB pin	V _{FB}			1.204		V
Soft-start charging current	I _{SS}			5		μA
VCC regulation	VCC	V _{BAT} = 3.6V, V _{OUT} = 12V, light load		5.7		V
		V _{BAT} = 3.6V, V _{OUT} = 12V, I _{LOAD} = 0.5A		5.3		V
Boost converter input current limit	I _L	R _{ILIM} = 130k		13		A
Boost converter frequency	f _{BOOST}			350		kHz

¹ Depending on parts and PCB layout, characteristics may be changed.

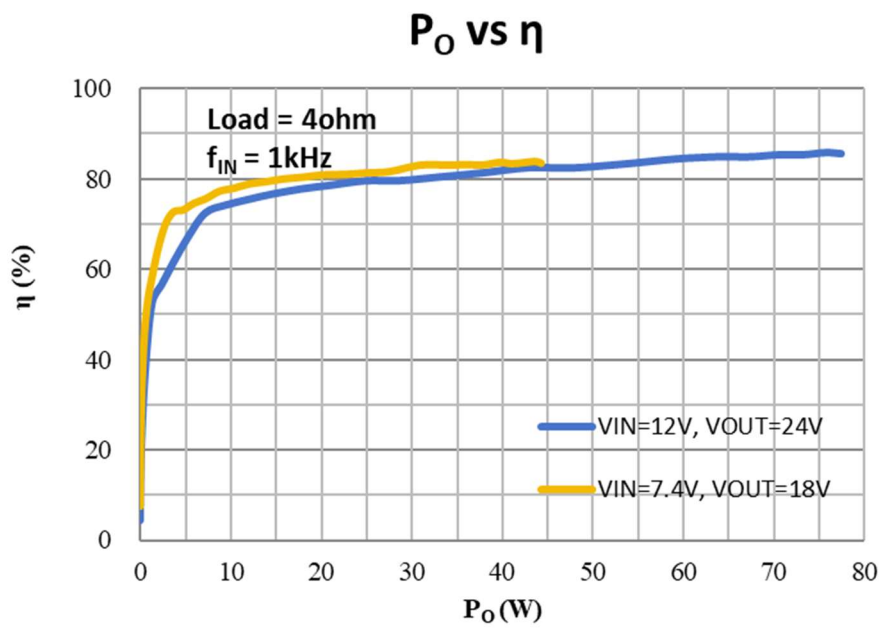
² Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability

Audio Amplifier

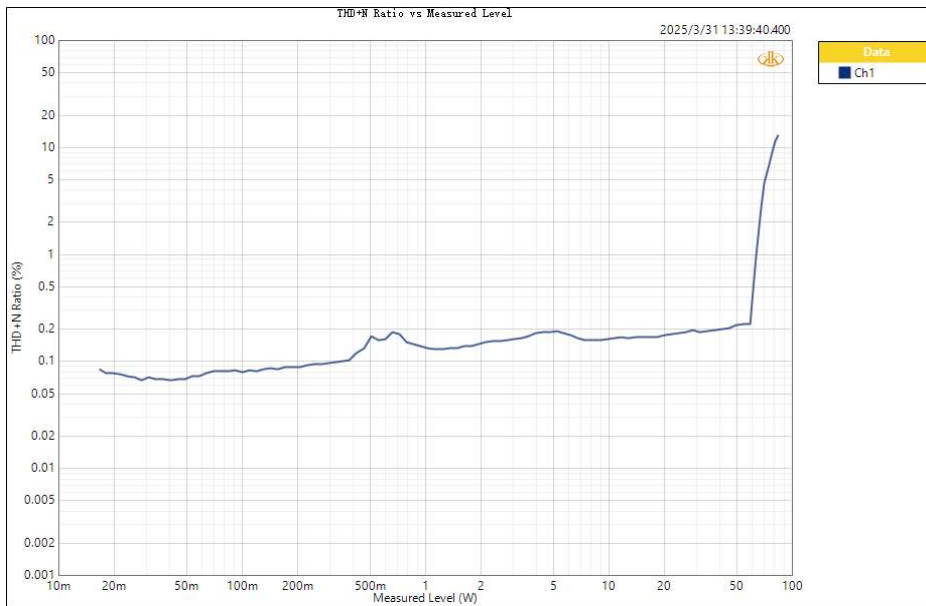
PARAMETER	Symbol	CONDITION	MIN	TYP	MAX	UNIT
Amplifier Output Offset Voltage	V_{OS}	$V_I = 0V$, Gain = 26dB		1.5		mV
Quiescent supply current in SD mode	I_{SD}	$V_{BAT} = 3.7V$		34		uA
		$V_{BAT} = 7.4V$		120		uA
Operating quiescent current	I_{BAT}	$V_{BAT} = 12V$, $PVDD = 18V$, $ENA=ENB=H$		15		mA
System Gain	Gain	GAIN=L		28		dB
		GAIN=H		36		dB
Turn-on time	t_{on}			190		ms
Turn-off time	t_{off}	Pull \SD low		5		us
Total harmonic distortion plus noise	THD+N	$P_o=1W$, $R_L=4\Omega$, $f=1kHz$		0.1		%
Noise output voltage	V_N	$f=20Hz\sim 20kHz$, A-weighted, 28dB		170		μV_{rms}
Class D switching frequency	$f_{Class-D}$			360		kHz
Spread frequency range				± 15		kHz
GVDD regulation	GVDD	$PVDD = 12V$, working on		4.9		V
		$PVDD = 12V$, shutdown		4.1		V
		$PVDD = 24V$, working on		4.9		V
		$PVDD = 24V$, shutdown		4.8		V
Over current trip point	OCP			12		A

Boost Converter + Class D

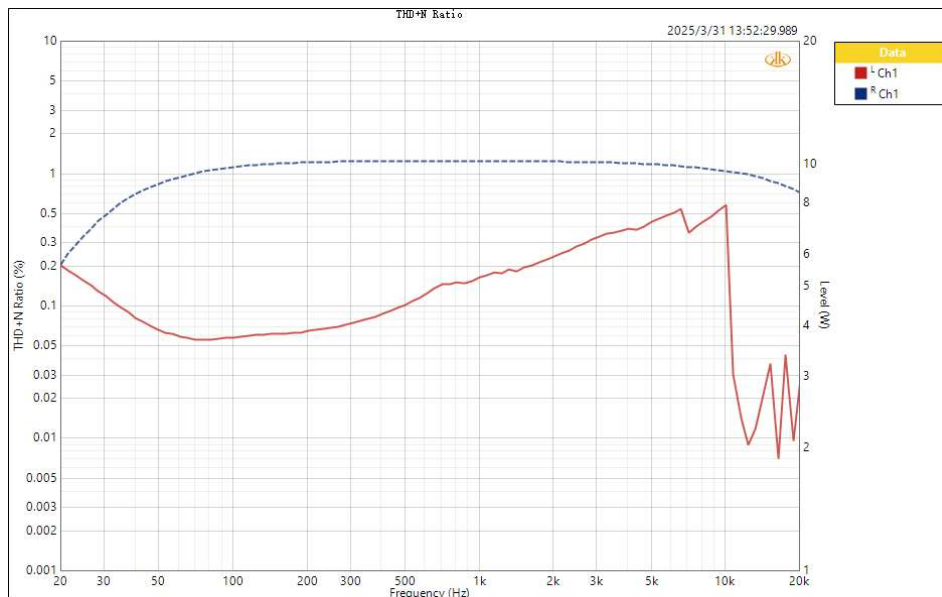
PARAMETER	Symbol	CONDITION	MIN	TYP	MAX	UNIT
Efficiency (Class D + Boost)	η	$V_{BAT} = 7.4V$, $PVDD = 18V$, $R_L=4\Omega$, $P_o = 2\times 40W$		83		%
		$V_{BAT} = 12V$, $PVDD = 24V$, $R_L=4\Omega$, $P_o = 70W$		85		
Thermal shutdown threshold	T_{SD}			150		$^{\circ}C$
Thermal shutdown hysteresis	T_{SD_HYS}			20		$^{\circ}C$

TYPICAL OPERATING CHARACTERISTICS


BTL mode, $R_L = 4\Omega$, $f_{IN} = 1\text{kHz}$, $V_{BAT} = 12\text{V}$, $P_{VDD} = 24\text{V}$, $R_{LIM} = 130\text{k}$, unless otherwise specified.

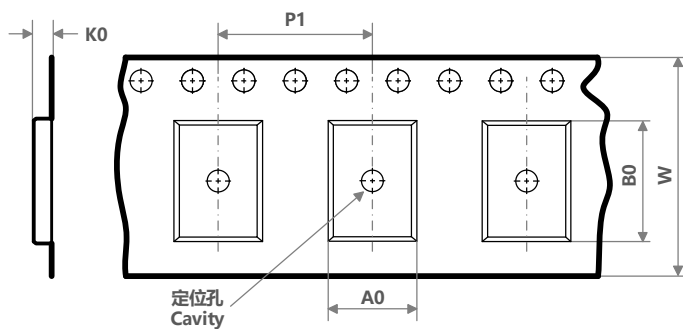
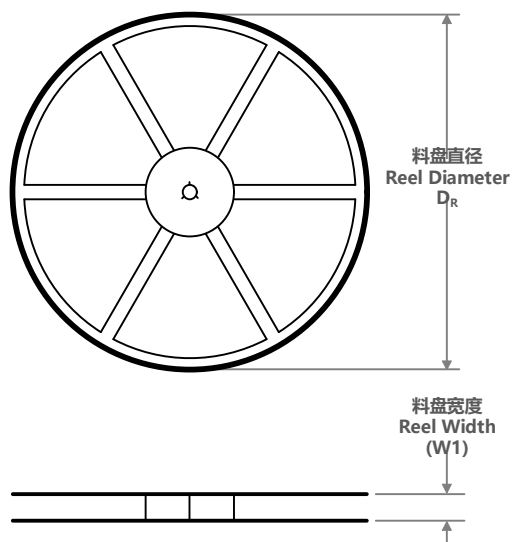


Output power vs THD+N



frequency vs THD+N
($P_o \approx 10\text{W}$)

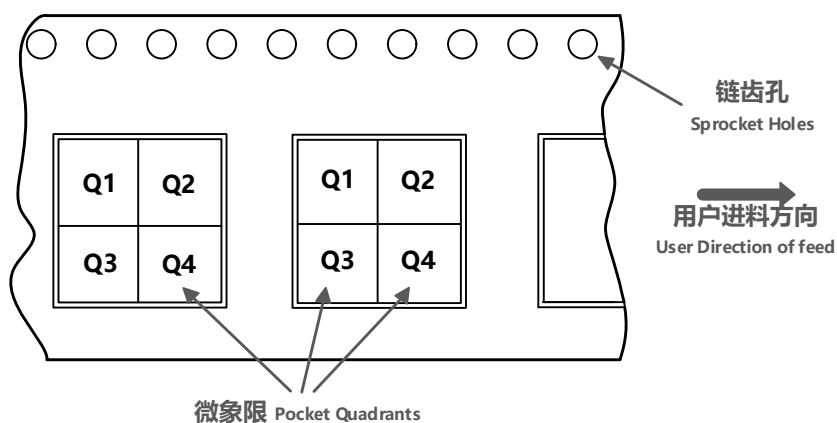
TAPE AND REEL INFORMATION



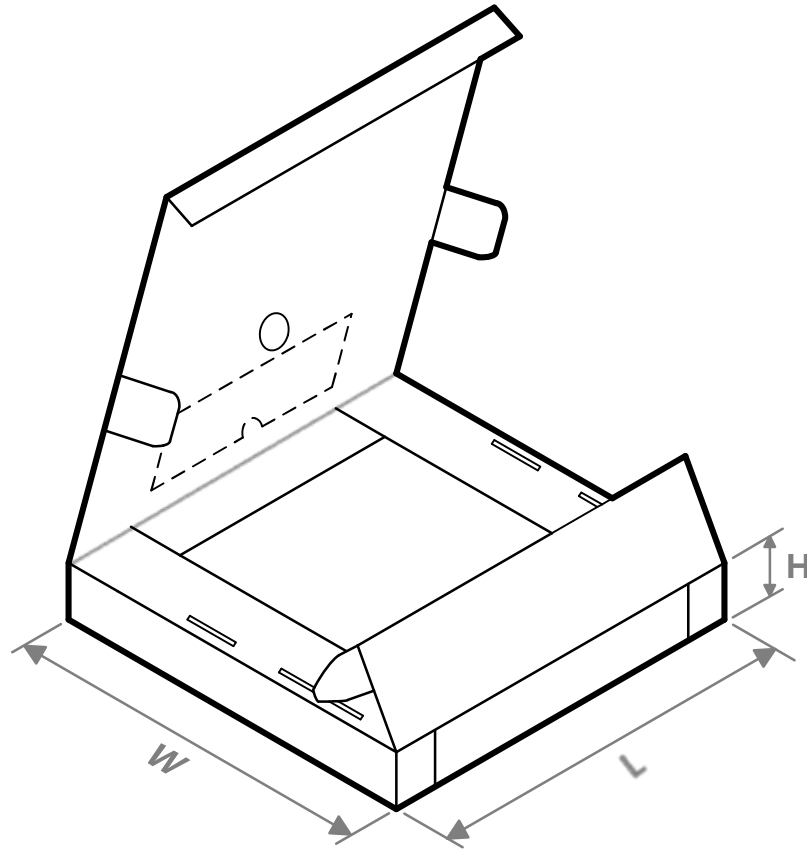
A0	Dimension designed to accommodate the component width; 料槽宽度
B0	Dimension designed to accommodate the component length; 料槽长度
K0	Dimension designed to accommodate the component thickness; 料槽厚度
W	Overall width of the carrier tape; 载带整体宽度
P1	Pitch between successive cavity centers; 相邻槽中心间距

编带 PIN1 方位象限分配

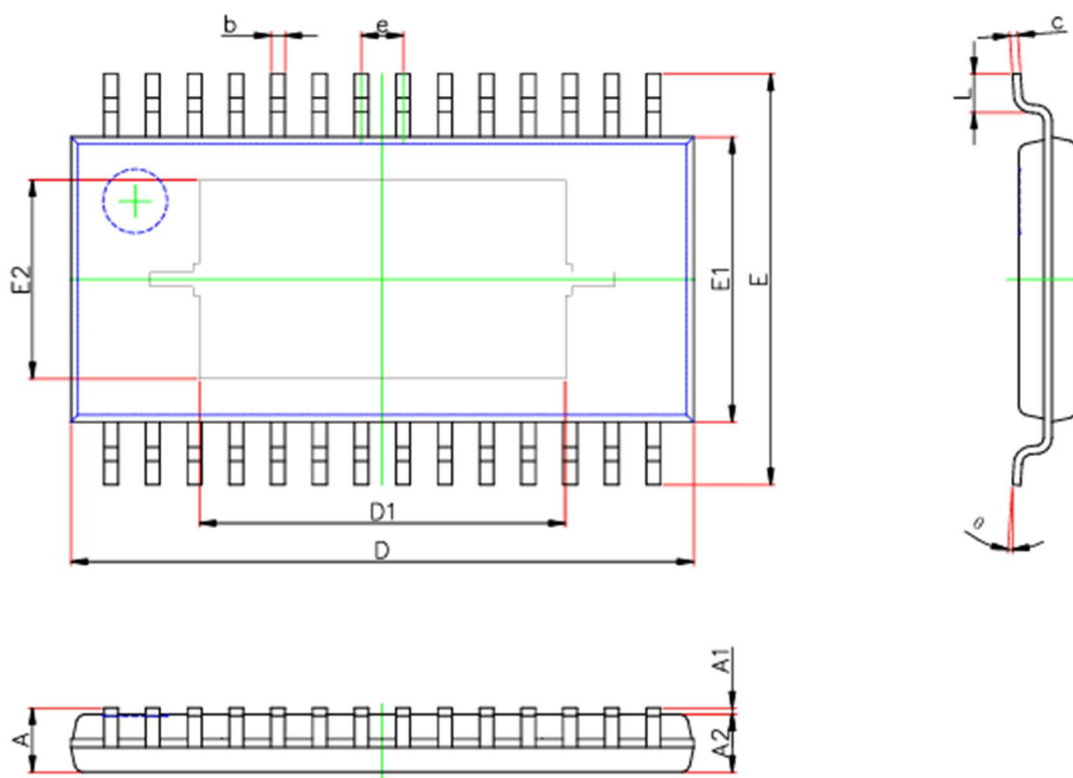
Quadrant Assignments for Pin1 Orientation in Tape



器件料号 Part No.	封装 类型 Package Type	封装 标识 Package Code	引脚 数 Pins	SPQ	料盘 直径 D_R (mm)	料盘 宽度 $W1$ (mm)	$A0$ (mm)	$B0$ (mm)	$K0$ (mm)	$P1$ (mm)	W (mm)	Pin1 象限 Quadrant
HTA8127MTER	ETSSOP (EP UP)	MTE	28	3000	330	16	6.7	9.9	1.5	12	16	Q1

■ TAPE AND REEL BOX INFORMATION


器件料号 Part No.	封装类型 Package Type	封装标识 Package Code	引脚数 Pins	SPQ	长度 Length (mm)	宽度 Width (mm)	高度 Height (mm)
HTA8127MTER	ETSSOP (EP UP)	MTE	28	6000	390	345	55

■ PACKAGE OUTLINE
MTE (ETSSOP28, EP UP)


SYMBOL	MILLIMETER	
	MIN	MAX
A	/	1.200
A1	0	0.150
A2	0.800	1.050
b	0.190	0.300
c	0.090	0.200
D	9.600	9.800
D1	5.600	5.800
E	6.250	6.550
E1	4.300	4.500
E2	3.000	3.200
e	0.650BSC	
L	0.450	0.750
Θ	0°	8°

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